

CLASSIFICATION:

UNCLASSIFIED

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A. MISSION DESCRIPTION AND BUDGET ITEM JUSTIFICATION:

The current program provides continuation of a series of progressive modular improvements which began with the S-3 Weapons System Improvement Program (WSIP) Phase I (S-3A modified to S-3B configuration). Based upon the S-3 WSIP Operational Requirement, the full program achieves the required multi-mission operational capability through continuous time-phased, selective mission avionics processing upgrades that are pursued in priority order. Initial Nunn-funded development focused on the Co-Processor Memory Unit (CPMU) hardware, a joint U.S./Canadian industrial base development program which provides the core processing capability and open architecture required for future modular S-3B modification. This program will complete CPMU integration and test and rewrite existing Tactical Mission Program (TMP) code into Ada higher-order language. Ada TMP development and CPMU integration will be leveraged to provide for sensor and connectivity enhancements in conjunction with the S-3B Surveillance System Upgrade (SSU) Project.

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